Faster Programs with Guile 3

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this talk

What? How? Where?

 Your programs are faster with Guile 3!

- The path to Guile 3
 Where?
- The road onward

► 2.7x as fast ► 1.5x as fast Guix graft a. as fast

results

- Guile 3 it's Guile, but faster!
- Sum 10 million element f32vector
- Expand (sxml ssax)
- And it will only get faster!

back the truck up

too slowly.

- In 2006, I had Guile programs that ran
- I did everything
- C hot-paths
- Extensive cacheing/memoizing
- **Built a profiler...**
- In the end, problem was: Guile ran Scheme code too slowly.
- Solution: make Guile faster.

Guile in 2006



Guile **n**





running bytecode

from bytecode tape machine

- At run-time: interpret instructions
- Bytecode interpreter: vm.c
- Like turing machine: bytecode is the
- Interpreter sometimes called "virtual"
- Defined on top of "native" machine (e.g. x86, C, ...)

but then

A faster Guile means more kinds of programs can be written in Guile
Also, I got hooked – making compilers is fun
✤ This is my job now

Guile **n**







current Guile needs

- Language needs to evolve
 Approach Racket (frontend work)
 Guile itself could be faster
- Enlarge set of Guile-appropriate problems
- Speed inception: speed up Guile,
 speed up compiler
- Spec
- Maintain low-latency programming
- I am a junkie

Guile **1n** 2019

memory



Guile **1n** 2019

improvement "Done"-ish

- (This is the Guile 3 work)
- Next step in incremental, compatible
- 2.9.1 released October 2018

Guile 3 goal

- Generate good native code
- Avoid code bloat
- Limit complexity of implementation
 - ---
- Keep support for all platforms
- Two steps:
- Lower-level bytecode
- Generate native code

lowerlevel bytecode

Guile 2.2:

0 2 3

```
scheme@(guile-user)> ,x (lambda (x) (vector-ref x 0))
        (assert-nargs-ee/locals 2 0)
        (vector-ref/immediate 0 0 0)
        (handle-interrupts)
        (return-values 2)
```

lowerlevel bytecode

Guile 3.0:

0

2

3

5

6

8

9

10

11

12

13

14

15

16

17

18

20

L1:

L2:

```
scheme@(guile-user)> ,x (lambda (x) (vector-ref x 0))
        (instrument-entry 229)
        (assert-nargs-ee/locals 2 0)
                                          ;; 2 slots (1 ar
        (\text{immediate-tag}=? 0 7 0)
                                          ;; heap-object?
                                          ;; -> L2
        (jne 15)
        (heap-tag=? 0 127 13)
                                           ;; vector?
        (jne 12)
                                           ;; -> L2
        (word-ref/immediate 1 0 0)
        (ursh/immediate 1 1 8)
        (imm - s64 < ? 1 0)
                                          ;; -> L1
        (jnl 5)
        (scm-ref/immediate 1 0 1)
                                           ;; 1 slot
        (reset-frame 1)
        (handle-interrupts)
        (return-values)
```

(make-short-immediate 1 2) ;; 0 (throw/value+data 1 177) ;; #(out-of-range ...)

(throw/value+data 0 201) ;; #(wrong-type-arg ...







compared to Guile 2.2

Instruction More of More of More of More with More with

- Instructions closer to machine code
- More instructions
- More control flow
- More optimization opportunities (e.g. elide type checks)
- More work for optimizer

compared to Guile 2.2

But...

- Compile time *could* be longer
- More instructions means more work for compiler
- Run time could be longer
- More instructions means more work at run-time for instruction dispatch

code generation

Interpreter:

* */ } Compiler:

/* make-short-immediate dst:8 low-bits:16

```
* Make an immediate whose low bits are
* LOW-BITS, and whose top bits are 0.
```

```
uint8 t dst;
scm t bits val;
```

```
UNPACK_8_16 (op, dst, val);
SP SET (dst, SCM PACK (val));
NEXT (1);
```

```
jit_movi (T0, SCM_UNPACK (val));
jit stxi (8 * dst, SP, T0);
```

code generation

architectures would

GNU Lightning: implementations of jit_movi, etc for all common architectures

Native code performs same operations on Guile stack that VM interpreter

No register allocation yet

Tier-up possible anywhere

Tier-down anywhere to debug

Complete JIT support in 5 kLOC

Only 1 reserved reg (current thread)

when: AOT?

time values

- Ahead-of-time (AOT) code generation perfectly possible
- Native code currently a pure function of bytecode, not specialized on run-
- Store result in ELF
- Not yet implemented

when: JIT?

- Just-in-time (JIT): generate native code at run-time
- But when, specifically?
- Need to avoid codegen for bytecode that doesn't matter
- Guile: per-function counter incremented at call and loop iteration Configurable tier-up threshold

platforms

status

- GNU Lightning impedance probs :(Lightning 1: Close! But limited
- Lightning 2: API good, but...
- Crashes in optimizer sometimes :(
- Do not want optimizer
- Regalloc useless for Guile
- Custom calling conventions hard
- Need solution before 3.0



proposal) others!)

- **Register allocation**
- Consistently comparable perf to Chez WASM backend! (Depends on "GC"
- Racketification
- (Figure out how I can play well with

questions?

@andywingo Happy hacking!

https://gnu.org/s/guile https://wingolog.org/ #guile on freenode

oh no it's the bonus slides

JIT environment variables

JIT.

- GUILE_JIT_THRESHOLD=50000: When to JIT; -1 for never, 0 for always
- Call increments by 2, loop by 30
- High default == JIT slow currently
- GUILE_JIT_LOG=O: Log level; up to 4.
- GUILE_JIT_STOP_AFTER=0: Stop JIT compilation after this many functions. Useful for debug.
- GUILE_JIT_PAUSE_WHEN_STOPPING=O: Pause for GDB to attach after stopping